Amendments to the Specification:

Amend the paragraph beginning at page 5, line 6 as follows:

--Non-Volatile Memory circuit 20 is connected to a ~~KVG-68~~

KGV-68 encryption device 24 which allows Non-Volatile Memory

circuit 20 to load a crypto key with its corresponding check word

into the encryption device 24. The encryption device is

connected to a telemeter transmitter 26 which transmits encrypted

telemetry data from an encryption device 24 to a ground

station.--

Amend the paragraph beginning at page 7, line 1 as follows:

--At this time it should be noted that the software of

Appendix A is adapted for processing two KGV-68 although only one

is illustrated in FIG. 1. In a security upgrade configuration

the software operates in a manner which allows two KGV-68

encryption units to be loaded with a crypto key and its

corresponding check word. It should be noted that while FIG. 1

only shows one ~~KVG-68~~ KGV-68, the non-volatile memory comprising

the present invention may be easily modified to accommodate to

~~KVG-68~~ KGV-68 encryption units.

Amend the paragraph beginning at page 7, line 22 as follows:

--The XMTR_DISABLE output from microprocessor 32 is set high during initialization to disable transmitter 26. The ENCR_SENSE_IN output from microprocessor 32 is set low during initialization indicating that the ~~KVG-68~~ <u>KGV-68</u> encryption device 24 is not being loaded. The ENCR_FCLK and ENCR_FDATA outputs from microprocessor 32 are set high during initialization. The clock signal provided by microcontroller 32 at the ENCR_FCLK output from micrcontroller 32 has an active falling edge necessitating that the signal be set high during initialization of micrcontroller 32. Setting the ENCR_FDATA output from microprocessor 32 high results in "0" at the ENCR_FDATA output of microprocessor 32.--